IN THE CLAIMS

Amend the claims as follows:

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1. (Amended) A ceramic passive component which comprises a carrier substrate (1),

at least one first electrode comprising a metal or alloy and (2) having a first surface disposed, on the substrate,

at least one dielectric (5) having a first surface disposed, on a second surface of the at least one first electrode opposing said first surface of the at least one first electrode, and at least one second electrode (6) disposed on a second surface of the at least one dielectric opposing said first surface of the at least one dielectric,

wherein the at least one dielectric (5) comprises a ferroelectric ceramic material with a voltage-dependent relative dielectric constant $\epsilon_{\rm r}$.

2. (Amended) A ceramic passive component as claimed in claim 1, wherein the ferroelectric ceramic material with a voltage-dependent dielectric constant c_r is a material selected from the group consisting of:

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 $\begin{array}{l} {\rm Pb}\,({\rm Zr}_{x}{\rm Ti}_{1-x})\, O_{3} & (0\,\leq\,x\,\leq\,1) \ \ {\rm with\ and\ without\ excess\ lead,\ } {\rm Ba}_{1-x}{\rm Sr}_{x}{\rm Ti}O_{3} \\ \\ (0\,\leq\,x\,\leq\,1)\,, \\ {\rm Pb}_{1-1.5y}{\rm La}_{y}\,({\rm Zr}_{x}{\rm Ti}_{1-x})\,O_{3} & (0\,\leq\,x\,\leq\,1,\ 0\,\leq\,y\,\leq\,0.2)\,, \ {\rm Pb}\,({\rm Zr}_{x}{\rm Ti}_{1-x})\,O_{3} & (0\,\leq\,x\,\leq\,1) \\ \end{array}$

1) doped with Nb, $Pb_{1-\alpha y}$ La_yTiO_3 (0 \leq y \leq 0.3, 1.3 \leq α \leq 1.5),

(Pb,Ca)TiO₃, BaTiO₃ with and without dopants, $SrZr_xTi_{1-x}O_3$ (0 \leq x \leq 1) with and without Mn dopants, $BaZr_xTi_{1-x}O_3$ (0 \leq x \leq 1), $SrTiO_3$ doped with, for example, La, Nb, Fe or Mn,

 $[Pb(Mg_{1/3}Nb_{2/3})O]_{x}-[PbTiO_{3}]_{1-x} (0 \le x \le 1),$

 $(Pb, Ba, Sr) (Mg_{1/3}Nb_{2/3})_x Ti_y (Zn_{1/3}Nb_{2/3})_{1-x-y}O_3 \ (0 \le x \le 1, \ 0 \le y \le 1, \ x + y \le 1), \ PbNb_{4/5x} ((Zr_{0.6}Sn_{0.4})_{1-y}Ti_y))_{1-x}O_3 \ (0 \le x \le 0.9, \ 0 \le y \le 1), \ (Ba_{1-x}Ca_x) TiO_3 \ (0 \le x \le 1),$

 $(Ba_{1-x}Sr_x)TiO_3$ $(0 \le x \le 1)$, $(Ba_{1-x}Pb_x)TiO_3$ $(0 \le x \le 1)$, $(Ba_{1-x}Sr_x)(Ti_{1-x}Zr_x)O_3$

 $(0 \le x \le 1, 0 \le y \le 1),$

 $Pb (Mg_{1/2}W_{1/2}) O_3$

Pb (Fe_{1/2}Nb_{1/2})O₃

Pb ($Fe_{2/3}W_{1/3}$) O_3

 $Pb(Ni_{1/3}Nb_{2/3})O_3$

Pb $(Zn_{1/3}Nb_{2/3})O_3$

Pb (Sc_{1/2}Ta_{1/2}) O₃

as well as combinations of the compounds a) to f) with PbTiO $_3$ and Pb(Mg $_{1/3}$ Nb $_{2/3}$)O $_3$ with and without excess lead.

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- 3. (Amended) A ceramic passive component as claimed in claim

 1, wherein the at least one first electrode (2) or the at least one second electrode (6) comprise(s) at least a first and a second electrically conducting layer.
- 4. (Amended) A ceramic passive component as claimed in claim 3, wherein the first electrically conducting layer of the at least first electrode(2) or of the at least one second electrode (6) comprises Ti, Cr, Ni_xCr_y (0 \leq x \leq 1, 0 \leq y \leq 1) or Ti_xW_y (0 \leq x \leq 1, 0 \leq y \leq 1).
- A ceramic passive component as claimed in claim 3, wherein the second electrically conducting layer of the at least one first electrode (2) or of the at least one second electrod (6) comprises a metal or an alloy.
- 6. (Amended) A ceramic passive component as claimed in claim 1, wherein the carrier substrate (1) comprises a ceramic material, a ceramic material with a glass planarization layer, a glass-ceramic material, a glass material, or silicon.
- 7. (Amended) A ceramic passive component as claimed in claim 1, wherein the at least one dielectric (5) comprises multiple layers.

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A ceramic passive component as claimed in claim 1, wherein a protective layer (7) of an inorganic material and/or an organic material is laid over the entire component.

A voltage-controlled oscillator with as its 9. (Amended) capacitive component a ceramic passive component which comprises a carrier substrate (1), at least one first electrode (2) comprising a metal or an allow having a first surface disposed, on the substrate at least one dielectric (5) having a first surface disposed, on a second surface, opposed to said first surface of the at least first electrode, and at least a second electrode (6) disposed on a second surface of the at least one dielectric, opposed to said first surface of the at least one dielectric, wherein the at least ϕ ne dielectric (5) comprises a ferroelectric ceramic material with a voltage-dependent relative dielectric constant ε_r .

10. (Amended) A filter with as its capacitive component a ceramic passive component which comprises a carrier substrate (1), at least one first electrode (2) comprising a metal or alloy and having a first surface disposed on the substrate at least one dielectric (5) having a first surface disposed on a second surface of the at least one first electrode opposed to said first surface and at least one second electrode (6) having a surface disposed on 18

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said second surface of the at least one dielectric wherein the at least one dielectric (5) comprises a ferroelectric ceramic material with a voltage-dependent relative dielectric constant $\epsilon_{\rm r}$.

11. (Amended) A delay line with as its capacitive component a ceramic passive component which comprises a carrier substrate (1), at least one first electrode comprising a metal or alloy and (2) having a first surface disposed on the substrate at least one dielectric (5) having a first surface disposed on a second surface of the one first electrode opposed to said first surface and at least one a second electrode (6) having a surface disposed on said second surface of the at least one dielectric wherein the at least one dielectric (5) comprises a ferroelectric ceramic material with a voltage-dependent relative dielectric constant $\epsilon_{\rm r}$.

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12. (Amended) The use of a ceramic passive component which comprises a carrier substrate (1), at least one first electrode (2) comprising a metal or alloy and having a first surface disposed on the substrate at least one dielectric (5) with a voltage-dependent relative dielectric constant ϵ_r having a second surface opposed to said first surface disposed on a second surface of the at least one first electrode opposed to said first surface and at least one second electrode (6) disposed on said second surface of the at least one dielectric as a capacitive component.